

EAST

10/722437

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	140221	pin and (wafer or substrate or disk or disc or semiconductor) and (lift or raise or lower or lowers or raises or elevate or elevates or lifts)	USPAT	2005/04/15 19:14	
2	BRS	L2	2559	1 and "414"/\$.ccls.	USPAT	2005/04/15 18:49	
3	BRS	L3	796	2 and (tray or cartridge or stack)	USPAT	2005/04/15 15:45	
4	BRS	L4	1	("5162047").PN.	US-PGPU B; USPAT; USOCR	2005/04/15 16:20	
5	BRS	L5	10	("5334257").URPN.	USPAT	2005/04/15 16:20	
6	BRS	L6	2594	1 and "118"/\$.ccls.	USPAT	2005/04/15 16:39	
7	IS&R	L7	1026	((414/937) or (414/938) or (414/797.4) or (414/797.5) or (414/797.9)).CCLS.	USPAT	2005/04/15 19:22	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
8	BRS	L8	32	("20020063119"   "20020134512"   "4431473"   "4790258"   "4842683"   "4863561"   "4870030"   "4906328"   "4940505"   "5018479"   "5106764"   "5180435"   "5215619"   "5300187"   "5314538"   "5328555"   "5332444"   "5350480"   "5403434"   "5622400"   "5716495"   "5821175"   "5950071"   "5963833"   "5966586"   "5994240"   "6124211"   "6140247"   "6187682"   "6189483"   "6273104"   "6368776").PN.	US- PGPU B; USPA T; USOC R	2005/04/15 16:44	
9	BRS	L9	8	("5034199"   "5612850"   "5677824"   "5695568"   "5848670"   "6036782"   "6068441"   "6077357").PN.	US- PGPU B; USPA T; USOC R	2005/04/15 17:10	
10	BRS	L10	3	("6231716").URPN.	USPA T	2005/04/15 17:12	
11	BRS	L11	35999	pin and (wafer or substrate or disk or disc or semiconductor) and (lift or raise or lower or lowers or raises or elevate or elevates or lifts) and (tray or carrier)	USPA T	2005/04/15 18:49	
12	BRS	L12	1038	11 and "414"/\$.cccls.	USPA T	2005/04/15 18:49	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
13	BRS	L13	6305	(wafer or substrate or disk or disc or semiconductor) with tray	USPA T	2005/04/15 18:51	
14	BRS	L14	61853	(wafer or substrate or disk or disc or semiconductor) with carrier	USPA T	2005/04/15 18:51	
15	BRS	L15	67204	13 or 14	USPA T	2005/04/15 18:51	
16	BRS	L16	2935	15 and ("118"/\$.ccls. or "414"/\$.ccls.)	USPA T	2005/04/15 18:52	
17	BRS	L17	1051	16 and pin	USPA T	2005/04/15 18:52	
18	BRS	L18	926	17 and (hole or opening)	USPA T	2005/04/15 18:52	
19	BRS	L19	950	17 and (lift or raise or lower or lowers or raises or elevate or elevates or lifts)	USPA T	2005/04/15 19:13	
20	IS&R	L20	1714	((118/729) or (118/728)).CCLS.	USPA T	2005/04/15 19:14	
21	BRS	L21	1298	20 and (wafer or substrate or disk or disc or semiconductor) and (lift or raise or lower or lowers or raises or elevate or elevates or lifts)	USPA T	2005/04/15 19:14	
22	BRS	L22	830	414/937 or 414/938	USPA T	2005/04/15 19:22	

	Current OR	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3
7	414/331.01	414/331.18; 414/754; 414/757; 414/935; 414/936		Nering; Eric A.	X				
8	414/783	414/331.01		Osaka; Akihiro et al.	X				
9	414/796.9	294/115; 414/739; 414/753.1; 414/801		Battrel; Joel	X				
10	414/810	206/710; 206/711; 211/41.18; 414/811; 414/937; 414/938		Pfeiffer; Ken et al.	X				
11	118/719	156/345.32; 414/217; 414/744.5; 414/744.6; 414/935; 414/937; 414/939; 414/941; 901/14; 901/15		Suda; Atsuhiko et al.	X				
12	187/272	118/728; 118/729; 187/406; 414/935		Salzman; Phil	X				
13	361/234	118/723R; 156/345.54; 279/128		Harashima; Keiichi et al.	X				